EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	511	((database or memory) with extensible with format$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:16
L2	497	(executable with image with format$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:04
L3	11	1 and L2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:04
L4	10	3 and @ad<"20020402"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:17
L5	10	1 and (executable with image) and @ad<"20020402"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:16
L6	15	1 and (load$3 with executable) and @ad<"20020402"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:06
L7	411	((database) with extensible with format$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:16
L8	10	7 and (executable with image) and @ad<"20020402"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:16

EAST Search History

L9	2992	((database or memory) with extensible)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:16
L10	10	9 and 2 and @ad<"20020402"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:17
L11	15	2 and (extensible with format$4) and @ad<"20020402"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:26
L12	35	(load$3 with executable with (image or program or software)) and (extensible with format$4) and @ad<"20020402"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:39
L13	5	(load$3 with executable with (image or program or software) with format$4) and (extensible with format$4) and @ad<"20020402"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:45
L14	29	(database with file with formats) and (extensible with format$4) and @ad<"20020402"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:45
L15	26	(load$3 with executable with (image or program or software)) and (extensible with format$4) and @ad<"20020402" and "29"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:41
L16	0	(load$3 with executable with (image or program or software)) and (extensible with format$4) and @ad<"20020402" and 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:41

EAST Search History

L17	0	(load$3 with executable with (image or program or software) with format$4) and (database with file with formats) and @ad<"20020402"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:45
S1	668	(formats with (determin$4 or database or storage or memory)) and (format with executable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:03
S2	751	(formats with (determin$4 or identif$3 or database or storage or memory)) and (format with executable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/28 14:43
S3	92	S2 and (load$3 with executable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:33
S4	2	"6738932".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/28 16:32
S5	1	"287076".apn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:45
S6	3	("6802006").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:20
S7	362	(executable with image with format)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:39

EAST Search History

S8	53	(executable with image with formats)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:33
S9	11	S8 and (load$3 with executable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:39
S10	7	S9 and @ad<"20020402"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/01 06:04
S11	11	S9 and @ad<"200200304"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:34
S12	5	S9 and @ad<"20020304"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:40
S13	497	(executable with image with format$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:55
S14	543	(executable with image with format$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:48
S15	123	S14 and (load$3 with executable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:55

EAST Search History

S16	89	S15 and os	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:40
S17	54	S16 and @ad<"20020304"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:56
S18	0	S17 and (image near formats)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:40
S19	11	S17 and (image with formats)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 06:38
S20	2	"090650".apn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:45
S21	88	(executable with image with os)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:48
S22	9	S14 and S21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:48
S23	3	S22 and @ad<"20020304"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:49

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S24	157	((database or libary or memory or storage) with executable with image with format$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 07:18
S25	46	S24 and (load$3 with executable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:55
S26	14	S25 and @ad<"20020304"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 05:56
S27	543	(executable with image with format$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 06:38
S28	123	S27 and (load$3 with executable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 06:38
S29	89	S28 and os	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 06:38
S30	54	S29 and @ad<"20020304"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 07:19
S31	8	S30 and (executable with formats)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 06:38

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S32	57	((database or libary or memory or storage) with executable with image with format$3) and os	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 07:18
S33	35	S32 and (executable with load$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 07:18
S34	10	S33 and @ad<"20020304"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 07:19
S35	17	S32 and @ad<"20020304"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/30 07:19

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The BSAVED **format** is a device-dependent raster **image format**; the file header **... image** that had been BSAVED than to **load** a BSAVED **executable** or data **image**. **...**
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[PPT] Interfacing ARM Assembly Language and C

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IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
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1 Kernel Korner
Alessandro Rubini
January 1998 **Linux Journal**
Publisher: Specialized Systems Consultants, Inc.
Full text available: html(19.42 KB) Additional Information: full citation, abstract, index terms

Playing with Binary Formats: This article explains how kernel modules can add new binary formats to a system and show a pair of examples

2 Joeq: a virtual machine and compiler infrastructure
John Whaley
June 2003 **Proceedings of the 2003 workshop on Interpreters, virtual machines and emulators IVME '03**
Publisher: ACM Press
Full text available: pdf(206.13 KB) Additional Information: full citation, abstract, references, citings, index terms

Joeq is a virtual machine and compiler infrastructure designed to facilitate research in virtual machine technologies such as Just-In-Time and Ahead-Of-Time compilation, advanced garbage collection techniques, distributed computation, sophisticated scheduling algorithms, and advanced run time techniques. Joeq is entirely implemented in Java, leading to reliability, portability, maintainability, and efficiency. It is also language-independent, so code from any supported language can be seamlessly ...

3 Implementing jalapeño in Java
Bowen Alpern, C. R. Attanasio, Anthony Cocchi, Derek Lieber, Stephen Smith, Ton Ngo, John J. Barton, Susan Flynn Hummel, Janice C. Sheperd, Mark Mergen
October 1999 **ACM SIGPLAN Notices , Proceedings of the 14th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications OOPSLA '99**, Volume 34 Issue 10
Publisher: ACM Press
Full text available: pdf(1.57 MB) Additional Information: full citation, abstract, references, citings, index terms

Jalapeño is a virtual machine for Java™ servers written in Java.A running Java program involves four layers of functionality: the user code, the virtual-machine, the operating system, and the hardware. By drawing the Java / non-Java boundary below the virtual machine rather than above it, Jalapeño reduces the boundary-crossing overhead and opens up more opportunities for optimization.To get Jalapeño started, a boot image of

a ...

4 Intel MMX for multimedia PCs

Alex Peleg, Sam Wilkie, Uri Weiser
January 1997 **Communications of the ACM**, Volume 40 Issue 1
Publisher: ACM Press

Full text available: pdf(3.15 MB) Additional Information: full citation, references, citings, index terms, review

5 Automatic logging of operating system effects to guide application-level architecture simulation

Satish Narayanasamy, Cristiano Pereira, Harish Patil, Robert Cohn, Brad Calder
June 2006 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the joint international conference on Measurement and modeling of computer systems SIGMETRICS '06/Performance '06**, Volume 34 Issue 1
Publisher: ACM Press

Full text available: pdf(251.41 KB) Additional Information: full citation, abstract, references, index terms

Modern architecture research relies heavily on application-level detailed pipeline simulation. A time consuming part of building a simulator is correctly emulating the operating system effects, which is required even if the goal is to simulate just the application code, in order to achieve functional correctness of the application's execution. Existing application-level simulators require manually hand coding the emulation of each and every possible system effect (e.g., system call, interrupt, D ...

Keywords: architecture simulation, checkpoints, emulating system calls

6 Transparent microprogramming in support of abstract type oriented dynamic vertical migration

Edward M. Carter, Robert I. Winner
December 1984 **ACM SIGMICRO Newsletter , Proceedings of the 17th annual workshop on Microprogramming MICRO 17**, Volume 15 Issue 4
Publisher: IEEE Press, ACM Press

Full text available: pdf(961.36 KB) Additional Information: full citation, abstract, references, citings, index terms

Migration of functionality into microcode for the purposes of performance improvement and increased security may be oriented toward the migration of abstract data types. This provides a model of migration consistent with current models of machine architecture. An advantage is that information in making migration decisions can be drawn from the programmer's model of his problem as encapsulated in the data types in the program. In addition, the migration can be sensitive to changes in type vi ...

7 MMLite: a highly componentized system architecture

Johannes Helander, Alessandro Forin
September 1998 **Proceedings of the 8th ACM SIGOPS European workshop on Support for composing distributed applications EW 8**
Publisher: ACM Press

Full text available: pdf(1.01 MB) Additional Information: full citation, citings, index terms

8 Operating systems: Run-time dynamic linking for reprogramming wireless sensor

networks
Adam Dunkels, Niclas Finne, Joakim Eriksson, Thiemo Voigt
October 2006 **Proceedings of the 4th international conference on Embedded networked sensor systems SenSys '06**
Publisher: ACM Press
Full text available: pdf(240.28 KB) Additional Information: full citation, abstract, references, index terms

From experience with wireless sensor networks it has become apparent that dynamic reprogramming of the sensor nodes is a useful feature. The resource constraints in terms of energy, memory, and processing power make sensor network reprogramming a challenging task. Many different mechanisms for reprogramming sensor nodes have been developed ranging from full image replacement to virtual machines.We have implemented an in-situ run-time dynamic linker and loader that use the standard ELF object fil ...

Keywords: dynamic linking, embedded systems, operating systems, virtual machines, wireless sensor networks

9 Binary translation
Richard L. Sites, Anton Chernoff, Matthew B. Kirk, Maurice P. Marks, Scott G. Robinson
February 1993 **Communications of the ACM**, Volume 36 Issue 2
Publisher: ACM Press
Full text available: pdf(4.84 MB) Additional Information: full citation, references, citings, index terms

Keywords: CISC computers, RISC computers, binary translation, computer architecture, processor architecture translation

10 GLEAM/1130-a production system base for computer-aided design
W. H. Sass
July 1968 **Proceedings of the 5th annual workshop on Design automation DAC '68**
Publisher: ACM Press
Full text available: pdf(698.55 KB) Additional Information: full citation, abstract, references, citings, index terms

The partitioned computing system, operating several tasks concurrently, is an effective tool for computer-aided design. Such a system offers a high-performance man-machine interface and at the same time operates the low-speed graphic devices (such as plotters) continuously with small drain upon the system. The concept of queueing tasks for devices, instead of queueing users for the system, improves user-access and maintains a reasonably constant load upon the system's hardware. Strong resid ...

11 Design of the real-time executive for the Univac(r) 418 system
John Michael Williams
January 1966 **Proceedings of the 1966 21st national conference**
Publisher: ACM Press
Full text available: pdf(1.06 MB) Additional Information: full citation, abstract, references, index terms

The UNIVAC 418 system hardware The 418 is a small- to medium-scale real-time computer announced to the general public in September of 1964. It is available in two models, identical except for storage speed (two or four microseconds). Storage and registers

12 Running on the bare metal with GeekOS

David Hovemeyer, Jeffrey K. Hollingsworth, Bobby Bhattacharjee
March 2004 **ACM SIGCSE Bulletin , Proceedings of the 35th SIGCSE technical symposium on Computer science education SIGCSE '04**, Volume 36 Issue 1
Publisher: ACM Press

Full text available: pdf(103.18 KB) Additional Information: full citation, abstract, references, citings, index terms

Undergraduate operating systems courses are generally taught using one of two approaches: *abstract* or *concrete*. In the abstract approach, students learn the concepts underlying operating systems theory, and perhaps apply them using user-level threads in a host operating system. In the concrete approach, students apply concepts by working on a real operating system kernel. In the purest manifestation of the concrete approach, students implement operating system projects that run on ...

Keywords: education, emulation, hardware, operating systems

13 Reconfigurable, retargetable bignums: a case study in efficient, portable Lisp system building
Jon L. White
August 1986 **Proceedings of the 1986 ACM conference on LISP and functional programming LFP '86**
Publisher: ACM Press
Full text available: pdf(1.70 MB) Additional Information: full citation, references, citings

14 Ontology-based metadata: transforming the MARC legacy
Peter C. Weinstein
May 1998 **Proceedings of the third ACM conference on Digital libraries DL '98**
Publisher: ACM Press
Full text available: pdf(1.21 MB) Additional Information: full citation, references, citings, index terms

15 Using common off-the-shelf tools to implement dynamic aspects
William R. Mahoney, William L. Sousan
February 2007 **ACM SIGPLAN Notices**, Volume 42 Issue 2
Publisher: ACM Press
Full text available: pdf(278.12 KB) Additional Information: full citation, abstract, references, index terms

An emerging software engineering paradigm, Aspect-Oriented Programming, can be used to facilitate moving common interests or requirements from individual software functions into a separate module. Aspect code is woven into the software on either a static (compilation) basis or dynamic (runtime) basis. Existing systems necessitate the use of syntactic sugar. which is added to programs to indicate the join points in the software where aspects could potentially be applied. Static weaving inserts ...

Keywords: GCC, dynamic aspects, event monitoring, intrusion detection, open-source

16 GRIP: graphics reduced instruction processor
Gautam B. Singh
September 1991 **Proceedings of the 24th annual international symposium on Microarchitecture MICRO 24**
Publisher: ACM Press
Full text available: Additional Information:

pdf(842.87 KB) full citation, references, index terms

17 A study of initialization in Linux and OpenBSD

Catherine Dodge, Cynthia Irvine, Thuy Nguyen
April 2005 **ACM SIGOPS Operating Systems Review**, Volume 39 Issue 2
Publisher: ACM Press
Full text available: pdf(2.02 MB) Additional Information: full citation, abstract, references, index terms

The code that initializes a system can be notoriously difficult to understand. In secure systems, initialization is critical for establishing a starting state that is secure. This paper explores two architectures used for bringing an operating system to its initial state, once the operating system gains control from the boot loader. Specifically, the ways in which the OpenBSD and Linux operating systems handle initialization are dissected.

18 A parallel processor system for three-dimensional color graphics

Haruo Niimi, Yoshirou Imai, Masayoshi Murakami, Shinji Tomita, Hiroshi Hagiwara
January 1984 **ACM SIGGRAPH Computer Graphics , Proceedings of the 11th annual conference on Computer graphics and interactive techniques SIGGRAPH '84**, Volume 18 Issue 3
Publisher: ACM Press
Full text available: pdf(892.77 KB) Additional Information: full citation, abstract, references, citings, index terms

This paper describes the hardware architecture and the employed algorithm of a parallel processor system for three-dimensional color graphics. The design goal of the system is to generate realistic images of three-dimensional environments on a raster-scan video display in real-time. In order to achieve this goal, the system is constructed as a two-level hierarchical multi-processor system which is particularly suited to incorporate scan-line algorithm for hidden surface elimination. The sys ...

19 Operating Systems: Program overlay techniques

R. J. Pankhurst
February 1968 **Communications of the ACM**, Volume 11 Issue 2
Publisher: ACM Press
Full text available: pdf(907.41 KB) Additional Information: full citation, abstract, references, citings

The general features of program overlay systems are described. Three main types—automatic, semiautomatic and non-automatic—are classified, and the programming techniques are explained as a function of machine hardware and other system features. The implementation of a semiautomatic overlay facility in a multiprogrammed system on the CDC 6600 is described in detail, with special reference to real time applications.

Keywords: loaders, multiprogramming, overlay techniques, storage allocation and segmentation

20 Architecture of a networked image search and retrieval system

R. Weber, J. Bollinger, T. Gross, H.-J. Schek
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Large scale networked image retrieval systems face a number of problems that are not fully satisfied by current systems. On one hand, integrated solutions that store all image

data centrally are often limited in terms of scalability and autonomy of data providers. On the other hand, WWW-based search engines proved to be fairly scalable, and data providers retain their autonomy. However, such engines often confront users with links to servers that are not available or to images that no longe ...

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